

Dyson School of Design Engineering

DE 1.3 Electronics Laboratory Oral Examination Guidelines

Peter Cheung, version 2.0

Assessment of Lab 1 - 4 will be in the form of a 15-minutes oral examination for each student on Thursday 8th of June 2017 from 2.00pm to 5.45pm. A schedule for the examination is shown below. **This is a formal assessment and you are REQUIRED to be present. Due to the tight schedule, please be in the Lab at least 15 minutes before your allotted time.**

The purpose of the Oral examination is to establish how much you have learned from these Lab Sessions. Questions will be asked which aim to establish your level of understanding and how well you have conducted the experiments including the effective use of your logbook. You are not required to memorize anything. You can bring along any materials, for example your notes and the laboratory instruction documents. **YOU MUST BRING YOUR ELECTRONIC LOGBOOK WITH YOU.** The Oral Examination will take place in EEE Lab, Level 1 EEE Building.

Your examiner will ask you questions from the four lab sessions, often requiring you to refer to your electronic logbook. He will then complete a feedback form with his assessment grade. A copy of the feedback/assessment form is attached here.

Oral Examination Schedule (in Room 104 EEE Lab)

Time	Examiner 1	Examiner 2	Examiner 3	Examiner 4
2.00pm - 2.15pm	Nirav	Ellie	Hugo	Robert
2.15pm - 2.30pm	Sophie	James	Rowan	Ryan
2.30pm - 2.45pm	Ben	Hayden	Hannah	Joe
2.45pm - 3.00pm	Jakob	Gordon	Morteza	Ollie
3.00pm - 3.15pm	Matthew	Omar	Aisha	Ruksana
3.15pm - 3.30pm	Matt	Federico	Michael	Alfie
3.30pm - 3.45pm	Emily	Tom	Bill	Oli
3.45pm - 4.00pm	Coffee		Break	
4.00pm - 4.15pm	Haron	Zach	Minal	Chris
4.15pm - 4.30pm	DanielY	Keith	Ric	Ken
4.30pm - 4.45pm	Neel	Gareth	Laerke	Thomas
4.45pm - 5.00pm	Christopher	DanielT	Pilar	Gabriele
5.00pm - 5.15pm	Justice	Anusha	Melisa	Jacob
5.15pm - 5.30pm	Visakan	Frankie	Fred	Sam